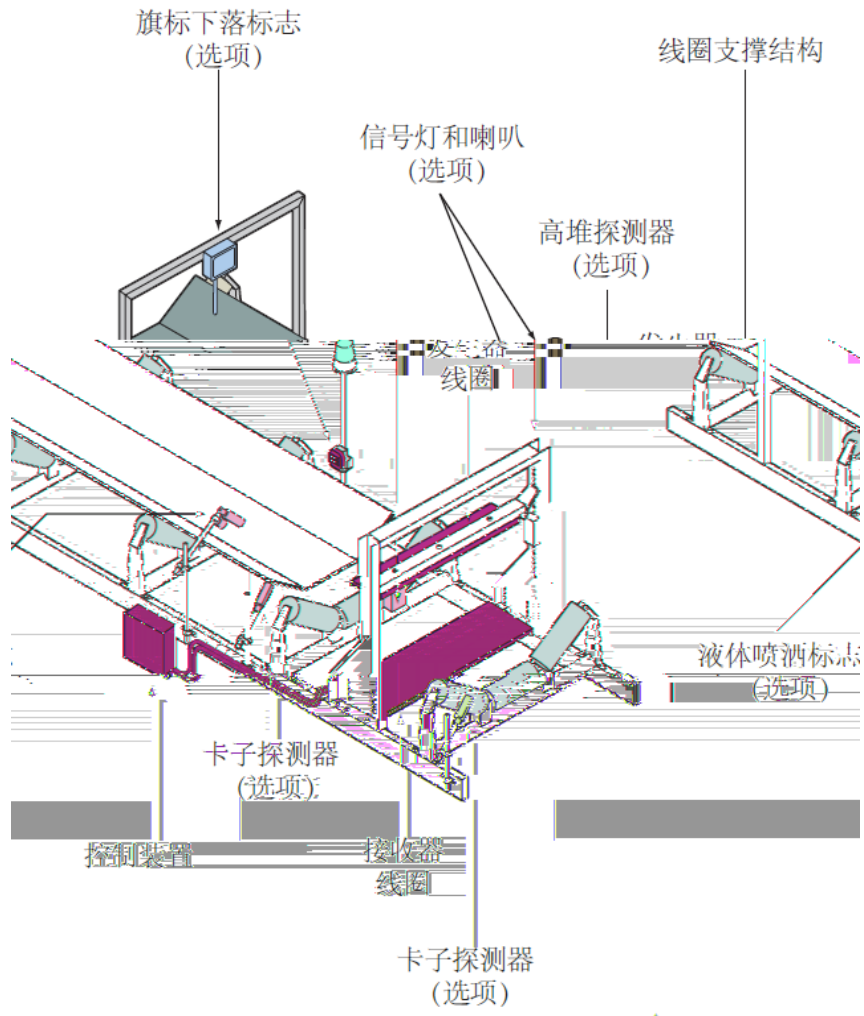


19 HQ-3



•
HQ-3

•

•

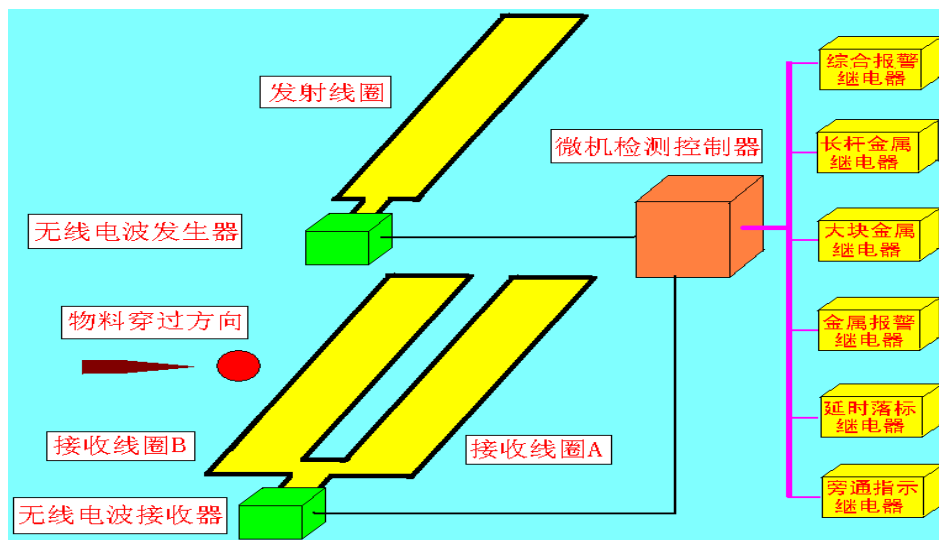
•

RS-485

• ADF

DSV

•



•

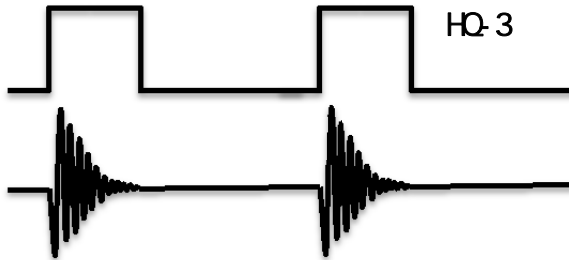
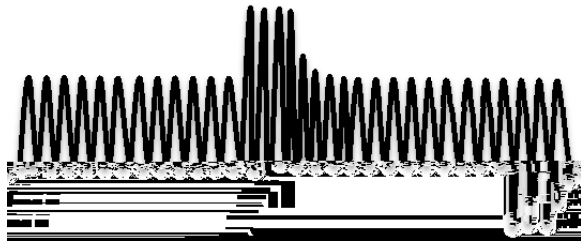
()

•

•

•

•



- ADF() DSV
- PNR QNR VMT
- 20%
- DSP FPGA
-

